



January 29th Meeting:

Real Life Charged Board ESD Events- The results of the work performed by Andrew Olney and Team at Analog Devices

Being Presented By: Jay Hamlin, Principal Product Engineer, ESD Control Program Manager, Medtronic Microelectronics Center, Tempe, Arizona.

Jay has 24 years experience in electronics manufacturing to include: Contamination Control Engineer (Honeywell Solid State Electronics), Photolithography Process Engineer (Intel Corp), Wafer Fabrication Yield Enhancement Engineer (Intel Corp), Product Quality/Reliability and Failure Analysis Engineer (Motorola SPS), Supplier Quality Engineer (Qualcomm and Orbital Sciences), and ESD Control Program Manager (Medtronic).

Bachelor of Science in Electrical Engineering, University of Colorado, Colorado Springs.

Active member of National ESD Association since 1994
 Active member of five of the National ESD Association's Standards Working Groups (Ionization, Automatic Handlers, Garments, Compliance Verification, and Packaging)
 Active member of the National ESD Association Standards Committee
 Member of the National ESD Association Symposium Steering Committee
 Current and Past A/V Chairman for the National ESD Association Symposium
 Senior Member of Institute of Environmental Sciences and Technology
 Past Member of the American Society for Quality Control (ASQC)
 Past Member of the Institute of Electrical and Electronic Engineers (IEEE)

Abstract of Presentation:

We all know that electronic components each have an individual ESD susceptibility level. These "threshold" or "withstand voltage" levels are most often represented by Human Body Model (HBM) or Charged Device Model (CDM) voltages. That is, the magnitude of discharge that a particular component can withstand before exhibiting signs of ESD damage. Andrew has proven and documented that once a component is mounted onto a printed wiring board, there is another threshold level of which the manufacturer needs to be aware of. Specifically, "Charged Board ESD Susceptibility". That is, how an electronic component with a known withstand voltage level is effected by the added capacitance of the printed wiring board to which it is mounted? This presentation also presents facts and photographs that show that as many as 50% of component failures diagnosed as electrical overstress (EOS), can be attributed to "charged board ESD events. Lastly, Andrew presents how to eliminate this kind of threat. In my opinion, this is a great presentation with plenty of supporting data and photographs.

January Meeting

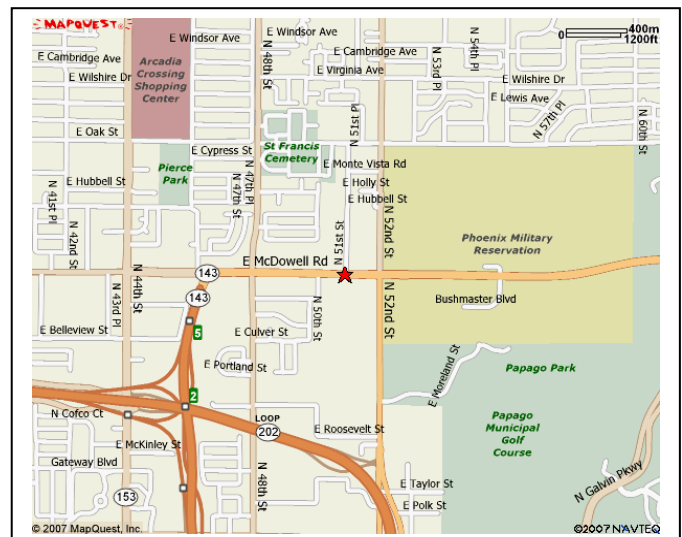
Date: Thursday January 29, 2009

Time: 6:30 p.m. Sign in & Social
 7:00 p.m. Speaker
 8:30 p.m. Close of meeting

Dinner Cost: Refreshments will be provided.

Location: **ON Semi Conductor** (See map below)
 5005 E McDowell Rd
 Phoenix, AZ 85008
 Ph: 602 244 3748

Please **RSVP by Tuesday January 27** at our website at www.southwestesd.com or call Chris Back at (602) 822 - 4229 (work), e-mail: christopher.back@honeywell.com.



Presidents Corner:

December’s meeting presentation and attendance was excellent. I like to thank Quantum Systems for the great dinner and Bill Butt’s for coordinating the meeting and dinner, great job. And finally again thank ON Semi-conductor for allowing us the use of their facility. Thank each of you for attending and you support.

Ron Roden
President Southwest ESD Association

Advertisers/Sponsors:

If you are interested in Advertising or providing sponsorship please visit our web site or contact any of the BoDs. The Southwest ESD association needs your support to continue operating and providing membership service.

Wanted: Speakers/Presenters

It is important to provide quality ESD presentation about, process controls, products, services and training to our members. If you are interested in doing a presentation or know of someone who would like to please contact any of the BoDs listed or email me.

EVENTS:

Symposiums/Conferences:

ESD events are provided by the National ESD Association for more information and registration, please visit their website at www.esda.org

Board of Directors Meeting (BOD)

Board of Directors meetings are held periodically, members are welcome to attend. Meetings will be posted on our web sites home page.

2009

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Programs

<u>Date</u>	<u>Location</u>	<u>Speaker/ Presentation</u>	<u>Presentation</u>
January 29, 2009, Thursday	ON-Semi Conductor	Jay Hamlin	Real Life Charged Board ESD Events
TBD	Tour – TASER	Tour to be scheduled	

Southwest ESD Association